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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/039,034	01/02/2002	Tony S. Rand	42390P12361	2349
8791	7590	07/01/2004	EXAMINER	
BLAKELY SOKOLOFF TAYLOR & ZAFMAN 12400 WILSHIRE BOULEVARD, SEVENTH FLOOR LOS ANGELES, CA 90025			LANE, JOHN A	
		ART UNIT		PAPER NUMBER
		2188		4
DATE MAILED: 07/01/2004				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/039,034	RAND, TONY S.
	Examiner	Art Unit
	Jack A Lane	2188

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 02 January 2002.
 2a) This action is **FINAL**. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-30 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1-30 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date _____.

- 4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
 5) Notice of Informal Patent Application (PTO-152)
 6) Other: _____.

DETAILED ACTION

1. Claims 1-30 are presented for examination.
2. The examiner requests, in response to this Office action, any reference(s) known to qualify as prior art under 35 U.S.C. sections 102 or 103 with respect to the instant claims. That is, any prior art (including any products for sale) similar to the instant claimed invention that could reasonably be used in a 102/103 rejection.
This request does not require applicant to perform a search. This request is not intended to interfere with or go beyond that required under 37 C.F.R. 1.56 or 1.105. This request may be fulfilled by asking the attorney(s) of record handling prosecution and the inventor(s)/assignee for references qualifying as prior art. A simple statement that the query has been made and no prior art found is sufficient to fulfill the request. Otherwise, the fee and certification requirements of 37 CFR section 1.97 are waived for those documents submitted in reply to this request. This waiver extends only to those documents within the scope of this request under 37 CFR, section 1.105 that are included in the application's first complete communication responding to this requirement. Any supplemental replies subsequent to the first communication responding to this request and any information disclosures beyond the scope of this request under 37 CFR section 1.105 are subject to the fee and certification requirements of 37 CFR section 1.97.

In the event prior art documentation is submitted a discussion of relevant passages,

figs. etc. is requested. **A response to this inquiry is greatly appreciated.**

The examiner also requests, in response to this Office action, support be shown for language added to the claims on amendment. That is, indicate support for newly added claim language by specifically pointing to page(s) and line no(s). in the specification and/or drawing figure(s). This will assist the examiner in prosecuting the application.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

4. Claims 1-30 are rejected under 35 U.S.C. § 102(e) as being anticipated by Mackenthun et al. (Pat. No. 6,374,332).

Mackenthun teaches the claimed “input-output interface” as circuitry including interface 370 shown in figure 4. The claimed “first address” corresponds to a read request on line 410. The claimed “address queue” corresponds to circuitry including write buffer logic 434, write request 2 logic 438

and write request 1 logic 454 for storing all pending write addresses. The claimed “second address of a pending transaction” corresponds to all pending write addresses. The claimed “address logic circuitry” corresponds to circuitry for comparing read request addresses to pending write addresses as discussed at col. 15, lines 10-15. The claimed “ownership pre-fetch circuitry” corresponds to circuitry including snoop logic circuitry 432. The claimed “transaction queue” corresponds to circuitry including read request register 412, write request buffer logic 434, write request 2 logic 438 and write request 1 logic 454. The claimed “memory device coupled with said transaction queue” corresponds to circuitry including storage device 1, storage device 2, tag ram 414 and/or data storage devices 419. The claimed “cache” corresponds to first level cache (FLC) 404 and/or second level cache (SLC) 360. The claimed “transaction bypass circuitry” could correspond to circuitry for accesing the third level cache (TLC) 310 and/or crossbar module (TCM) 220 for accessing main storage 110.

The examiner believes all of the dependent claim features are taught by Mackenthun. That is, the more well known claim features commonly found in the dependent claims are explicitly or inherently found in Mackenthun. For example, claim 4 discusses invalidation. Invalidating data is discussed throughout Mackenthun (see the background of the invention and detailed description) and is generally a well known feature of cache coherency. Many of the claim features, while part of the invention, appear to be well known and their relevance not

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essential to the main invention found in the independent claim(s). Thus, a detailed discussion of the well known claim feature(s) is not warranted at this time.

Any response to this action should be mailed to:

Under Secretary of Commerce for Intellectual Property and
Director of the United States Patent and Trademark Office
PO Box 1450
Alexandria, VA 22313-1450

or faxed to:

(703) 872-9306, (for Official communications intended for entry)

Or:

(703) 872-9306, (for Non-Official or draft communications, please label "Non-Official" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA., Sixth Floor (Receptionist).

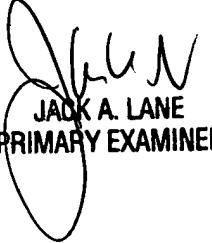
Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jack A. Lane whose telephone number is 703 305-3818. The examiner can normally be reached on Mon-Fri from 7:30AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on 703 306-2903.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703 305-3900.



JACK A. LANE
PRIMARY EXAMINER